



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Arnold, et al.

Serial No.

Filing Date: 08/07/2003

Confirmation No.:

Examiner:

Docket No. 10971189-4

For: **PROCESSING SYSTEM AND METHOD FOR EFFICIENTLY ENABLING  
DETECTION OF DATA HAZARDS FOR LONG LATENCY INSTRUCTIONS**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

This information disclosure statement is filed in accordance with 37 C.F.R. §§ 1.56, 1.97, and 1.98. Listed on the accompanying form PTO-1449 are documents which may or may not be considered material to the examination of this application. These documents were made of record in a prior application filed January 24, 2000 and having serial no. 09/490,392, to which priority is claimed relative to the present application pursuant to 35 U.S.C. §120. Copies of these documents are not being provided pursuant to 37 C.F.R. § 1.98(d)(1) and (2), which provides:

(d) A copy of any patent, publication, pending U.S. application or other information, as specified in paragraph (a) of this section, listed in an information disclosure statement is required to be provided, even if the patent, publication, pending U.S. application or other information was previously submitted to, or cited by, the Office in an earlier application, unless:

(1) The application is properly identified in the information disclosure statement and is relied on for an earlier effective filing date under 35 U.S.C. §120; and

(2) The information disclosure statement submitted in the earlier application complies with paragraphs (a) through (c) of this section.

The following rights are reserved by the Applicant(s): the right to establish the patentability of the claimed invention over any of the listed documents should they be applied as reference, the right to prove that some of these documents may not be prior art, and/or the right to prove that some of these documents may not be enabling for the teachings they purport to offer.

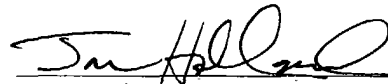
This statement should not be construed as a representation that an exhaustive search has been made, or that information more material to the examination of the present application does not exist. The Examiner is specifically requested not to rely solely on the materials submitted herewith. The Examiner is requested to conduct an independent and thorough review of the documents, and to form independent opinions as to their significance.

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449 to indicate the documents have been considered.

Respectfully submitted,

**THOMAS, KAYDEN, HORSTEMEYER  
& RISLEY, L.L.P.**

By:



**Jon E. Holland, Reg. No. 41,077**  
256-704-3900, Ext. 103

Intellectual Property Administration  
P.O. Box 272400  
Fort Collins, Colorado 80527-2400  
Docket: 10971189-4

**PATENT APPLICATION**

Sheet 1 of 1

FORM PTO-1449

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

**10971189-4**

APPLICATION NO.

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**08/7/2003**

GROUP

**REFERENCE DESIGNATION****U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	5,859,999	01/12/1999	Morris, et al.	
	1B	5,860,017	01/12/1999	Sharangpani, et al.	
	1C	5,737,624	04/1998	Garg, et al.	
	1D	4,903,196	02/1990	Pomerene, et al.	
	1E	4,891,753	01/1990	Budde, et al.	
	1F	6,266,766	07/2001	O'Conner, Dennis M.	
	1G				
	1H				
	1I				
	1J				
	1K				

**FOREIGN PATENT DOCUMENTS**

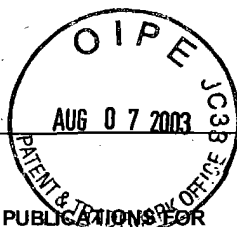
		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

	1Q	Alexander Wolfe, "Patents Shed Light on Merced: Techniques of Predication and Speculation Detailed," Electronic Engineering Times, February 15, 1999, pp. 43-44.
	1R	Patterson, et al., "Computer Architecture: A Quantitative Approach," Morgan Kaufmann Publishers, Inc., 2ed, pp. 150-193.
	1S	Gary Lauterbach, "Sun's Next-Generation High-End SPARC Microprocessor Forum, October 14-15, 1997, pp. 3-6.

EXAMINER

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Sheet 1 of 1

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	1L					
	1M					
	1N					
	1O					
	1P					

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

	1Q	William J. Bowhill, et al., "Circuit Implementation of a 300-MHz 64-bit Second-generation CMOS Alpha CPU," Digital Technical Journal, Vol. 7 No. 1, 1995, pp. 100-114.
	1R	
	1S	

EXAMINER

DATE CONSIDERED